


I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.


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REMARKS

Claims 1-22 are pending in this application, with claims 1, 4, 7, 10, 13, 16, 19 and 21 being independent. Claims 1, 4, 7, 10, 13 and 16 have been amended to recite that a gate electrode is formed adjacent to a semiconductor layer with a gate insulating film interposed therebetween and that the semiconductor layer includes needle-shaped or column-shaped crystals which are substantially aligned in a direction parallel to a length direction of a channel formation region, and claims 19-22 have been added. Support for these amendments may be found, for example, at page 20, lines 1-4 and page 12, line 23 to page 13, line 3.

Claims 1-18 were rejected for obviousness-type double patenting as being unpatentable over claims 1, 11 and 21 of U.S. Patent No. 6,365,933. Applicant submits that this rejection is obviated by the terminal disclaimer that is being submitted with this amendment.

Applicant submits that all claims are in condition for allowance.

Enclosed is a \$212 check for excess claims fees. Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

Date: _____

10/6/04



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